# Counting the Number of Fault Patterns in Redundant VLSI Arrays* 

Linda PAGLI ${ }^{\dagger}$ and Geppino $P U C C I^{\ddagger}$


#### Abstract

In VLSI technology, redundancy is a commonly adopted technique to provide reconfiguration capabilities to regular architectures. This paper proves upper and lower bounds on the number of minimal fault patterns (minimal set of faulty processors) which affect a linkredundant linear array in an unrepairable way, for both the cases of bidirectional and unidirectional links.


Keywords Distributed Systems, Fault Tolerance, Parallel Processing, Performance Evaluation.

## 1 Preliminaries

A standard technique to lower the production costs of VLSI circuits is the provision of on-chip redundancy, and accompanying mechanisms to reconfigure the chip components at the occurrence of fabrication faults. Without the presence of reconfiguration capabilities, the yield of very large VLSI chips would be so poor to make their production unacceptable.

In the case of linear arrays of identical Processing Elements (PE's), redundant ones (called spares) are often placed on the chip to replace faulty PE's and therefore preserve the network connectivity. Besides the regular links between neighboring PE's, extra links (called bypass links) are also included to recreate the array topology in the reconfiguration phase [1, 2]. However, regardless of any amount of redundancy and configuration capabilities, there are always sets of faults occurring at strategic positions which affect the chip in an unrepairable way [6]. Such sets of faults, called Catastrophic Fault Patterns (CFP's), have been extensively studied in $[4,5,6]$ for linear arrays with different varieties of link redundancy.

In this paper we provide upper and lower bounds on the number of CFP's for a special class of redundant arrays, where each PE has exactly one bypass link and where the links can either be unidirectional or bidirectional. As we show in a later section, such bounds can be used in assessing the effectiveness and the cost of the reconfiguration process.

[^0]Formally, let $A=\left\{p_{0}, p_{1}, \ldots, p_{N-1}\right\}$ denote a linear array of PE's (including both regular and spare elements), which are connected by regular links ( $p_{i}, p_{i+1}$ ), $0 \leq i \leq N-2$, and by bypass links $\left(p_{i}, p_{i+g}\right), 0 \leq i \leq N-1-g$. This interconnection, referred to as linear array with link redundancy $g$, will be the object of our analysis in the following sections. At the two ends of the array, two special PE's, called $I$ (for Input) and $O$ (for Output), are responsible for the I/O functions of the system. We assume that $I$ is connected to $p_{0}, p_{1}, \ldots, p_{g-1}$, while $O$ is connected to $p_{N-g}, p_{N-g+1}, \ldots, p_{N-1}$, so that all PE's in the system have the same degree and reliability bottlenecks at the borders of the array are avoided. Furthermore, we will conduct our analysis under the assumption that only PE's $p_{0}, p_{1}, \ldots, p_{N-1}$ can be faulty, while links and I/O nodes always operate correctly.

In [6], faults in the system are characterized as follows.

Definition 1 For a linear array of size $N$ and any link redundancy, a fault pattern $F$ starting at $p_{f_{0}}$ is a set of integers $F=\left\{f_{0}, f_{1}, \ldots, f_{m-1}\right\}$, where $0 \leq f_{i} \leq N-1$ and $f_{i-1}<f_{i}, 1 \leq i \leq m \leq N$.

Definition 2 Given a link-redundant linear array A, a fault pattern $F=\left\{f_{0}, f_{1}, \ldots, f_{m-1}\right\}$ is catastrophic for $A$ if and only if no path exists between $I$ and $O$, once the faulty $p_{i}, i \in F$, and their incident links are removed.

The occurrence of a CFP implies that there is no way of reconfiguring the system with respect to I/O operations.

It can be easily shown that, for any array $A$ with link redundancy $g$, a CFP $F$ for $A$ must contain at least $g$ faults. From now on, our analysis will concentrate on such minimal case. In this case, the width $W_{F}$ of a fault pattern $F=\left\{f_{0}, f_{1}, \ldots, f_{g-1}\right\}$ is defined to be the number of PE's between and including the first and the last fault in $F$, that is, $W_{F}=f_{g-1}-f_{0}+1$.

Theorem 1 ([6]) Let $F=\left\{f_{0}, f_{1}, \ldots, f_{g-1}\right\}$ be a fault pattern for a linear array $A$ with link redundancy $g$. Necessary condition for $F$ to be catastrophic is

$$
g \leq W_{F} \leq W_{F}^{B}=\left(\left\lceil\frac{g}{2}\right\rceil-1\right) g+\left\lfloor\frac{g}{2}\right\rfloor+1,
$$

in the case of bidirectional links and

$$
g \leq W_{F} \leq W_{F}^{U}=(g-1)^{2}+1,
$$

in the case of unidirectional links.
From the above theorem it easily follows that all CFP's $F=\left\{f_{0}, f_{1}, \ldots, f_{g-1}\right\}$ starting at $p_{i}$, that is, with $f_{0}=i$, are such that $f_{g-1} \leq i+W_{F}^{x}-1$, where $x \in\{B, U\}$ depends on the link orientation.

Starting from a result in [4], the following section provides very tight upper and lower bounds on the number of minimal CFP's for the bidirectional case. In Section 3 we prove an upper bound on the number of unidirectional CFP's by establishing a correspondence between the unidirectional and bidirectional case. The bounds are finally used in in Section 4 to determine a measure relevant to the cost assessment of the production process of the VLSI chip.

## 2 The case of bidirectional links

Nayak in [4] develops a technique to count the number of minimal CFP's in the case of bidirectional linear arrays with link redundancy $g$. Namely, he shows that there is a bijection between minimal CFP's starting at any fixed $p_{i}$, with $0 \leq i \leq N-W_{F}^{B}$, and the language $L$ of strings of length $g-1$ over the alphabet $\Sigma=\{(),, \star\}$, corresponding to balanced parenthesizations of $\star$ 's (e.g., for $g-1=3, L=\{\star \star \star, \star(),(\star),() *\})$. By known combinatorial facts [3], we then have:

$$
|L|=\mathrm{F}^{B}(g)=\sum_{j=0}^{\left\lfloor\frac{g-1}{2}\right\rfloor}\binom{g-1}{2 j}\binom{2 j}{j} \frac{1}{j+1}
$$

The remainder of this section is devoted to prove tight lower and upper bounds on the above quantity. We will make use of the following facts:

Fact 1 For any $j>0$,

$$
e^{-\frac{1}{6 j}} \frac{2^{2 j}}{\sqrt{\pi j}}<\binom{2 j}{j}<\frac{2^{2 j}}{\sqrt{\pi j}}
$$

Fact 2 For any $g>0$,

$$
\sum_{j=o}^{\left\lfloor\frac{g-1}{2}\right\rfloor}\binom{g-1}{2 j} 2^{2 j}=\frac{3^{g-1}+(-1)^{g-1}}{2}
$$

Fact 3 For any $n>0$,

$$
\sum_{k=0}^{n}\binom{n}{k} \frac{2^{k+1}}{k+1}=\frac{3^{n+1}-1}{n+1}
$$

Fact 1 can be easily proven by applying Stirling's approximation to $\binom{2 j}{j}$. The formulae in Facts 3 and 2 can be derived by means of standard techniques for finite summations [3].

Theorem 2 For any $g>1$,

$$
F^{B}(g)>\frac{e^{-\frac{1}{6}}}{2 \sqrt{\pi}} \frac{3^{g-1}+1}{\left\lfloor\frac{g+1}{2}\right\rfloor^{3 / 2}} .
$$

Proof: We have:

$$
\mathrm{F}^{B}(g)=\sum_{j=0}^{\left\lfloor\frac{g-1}{2}\right\rfloor}\binom{g-1}{2 j}\binom{2 j}{j} \frac{1}{j+1}>1+\frac{e^{-1 / 6}}{\sqrt{\pi}\left\lfloor\frac{g+1}{2}\right]^{3 / 2}} \sum_{j=1}^{\left\lfloor\frac{g-1}{2}\right\rfloor}\binom{g-1}{2 j} 2^{2 j}
$$

by applying Fact 1 and since, for $x \geq 1, \mathrm{f}(x)=e^{-\frac{1}{6 x}}$ increases and $\mathrm{g}(x)=\frac{1}{(x+1)^{3 / 2}} \leq \frac{1}{\sqrt{x}(x+1)}$ decreases. For odd values of $g$ we can then establish that

$$
\mathrm{F}^{B}(g)>\frac{e^{-1 / 6}}{\sqrt{\pi}\left(\frac{g+1}{2}\right)^{3 / 2}} \sum_{j=0}^{\frac{g-1}{2}}\binom{g-1}{2 j} 2^{2 j}
$$

For $g$ even, since $2 \frac{e^{-1 / 6}}{\sqrt{\pi}\left\lfloor\frac{g+1}{2}\right\rfloor^{3 / 2}}<1$, we can write:

$$
\mathrm{F}^{B}(g)>\frac{e^{-1 / 6}}{\sqrt{\pi}\left\lfloor\frac{g+1}{2}\right\rfloor^{3 / 2}}\left(1+\sum_{j=0}^{\left\lfloor\frac{g-1}{2}\right\rfloor}\binom{g-1}{2 j} 2^{2 j}\right)
$$

The theorem then follows from Fact 2 and the two above inequalities.
Corollary $1 \quad F^{B}(g) \in \Omega\left(\frac{3^{g}}{g^{3 / 2}}\right)$.

Theorem 3 For any $g>1$,

$$
F^{B}(g)<\frac{4\left(3^{g}-1\right)}{\sqrt{\pi} g\left(\left\lceil\frac{g-1}{2}\right\rceil+2\right)^{1 / 2}}+1
$$

Proof: We will only consider odd values of $g$, that is, $g=2 i+1$ for $i \geq 1$. The proof for $g$ even follows the same lines. From Fact 1, we have

$$
\mathrm{F}^{B}(g)=\mathrm{F}^{B}(2 i+1)<1+\sum_{j=1}^{i}\binom{2 i}{2 j} \frac{2^{2 j}}{\sqrt{\pi j}(j+1)}<1+\sqrt{\frac{2}{\pi}} \sum_{j=0}^{i}\binom{2 i}{2 j} \frac{2^{2 j}}{(j+1)^{3 / 2}}
$$

Note that the function $\mathrm{f}(x)=\frac{2^{2 x}}{(x+1)^{3 / 2}}$ is strictly increasing for $x \geq 1$. Therefore, for $0 \leq j<\left\lceil\frac{i}{2}\right\rceil$, we can bound the term $\binom{2 i}{2 j} \frac{2^{2 j}}{(j+1)^{3 / 2}}$ with the term $\binom{2 i}{2(i-j)} \frac{2^{2(i-j)}}{(i-j+1)^{3 / 2}}$. Hence,

$$
\sum_{j=0}^{i}\binom{2 i}{2 j} \frac{2^{2 j}}{(j+1)^{3 / 2}}<2 \sum_{j=\left\lceil\frac{i}{2}\right\rceil}^{i}\binom{2 i}{2 j} \frac{2^{2 j}}{(j+1)^{3 / 2}}<\frac{2 \sqrt{2}}{(i+2)^{1 / 2}} \sum_{j=0}^{i}\binom{2 i}{2 j} \frac{2^{2 j+1}}{2 j+1}
$$

The last summation in the above formula adds only and all the even terms of the summation described in Fact 3. Therefore,

$$
\mathrm{F}^{B}(2 i+1)<\frac{4\left(3^{2 i+1}-1\right)}{\sqrt{\pi}(i+2)^{1 / 2}(2 i+1)}+1
$$

The theorem follows.

Corollary $2 F^{B}(g) \in \Theta\left(\frac{3^{g}}{g^{3 / 2}}\right)$.

## 3 The case of unidirectional links

In order to prove bounds on the number of CFP's in the unidirectional case, we need to recall the following matrix representation of any fault pattern $F$ starting a fixed $p_{i}$, with $0 \leq i \leq N-W_{F}^{x}$, introduced in [6] for both the bidirectional and unidirectional case. $F$ is represented as a boolean matrix $W$ of size $\bar{W}_{F}^{x} \times g$, with $\bar{W}_{F}^{x}=\left\lceil\frac{W_{F}^{x}}{g}\right\rceil$, defined as follows:

$$
W[h, k]= \begin{cases}1 & \text { if }(i+h g+k) \in F \\ 0 & \text { otherwise }\end{cases}
$$

Under the above representation, each $f_{j} \in F$ is mapped into $W\left[h_{j}, k_{j}\right]=1$, where $h_{j}=\left\lfloor\frac{\left(f_{j}-i\right)}{g}\right\rfloor$ and $k_{j}=\left(f_{j}-i\right) \bmod g$. For instance, the boolean matrix associated to the fault pattern $F=$ $\{0,4,6,8,12\}$ for a linear array with link redundancy 5 is the following:

$$
\left[\begin{array}{lllll}
1 & 0 & 0 & 0 & 1  \tag{1}\\
0 & 1 & 0 & 1 & 0 \\
0 & 0 & 1 & 0 & 0
\end{array}\right] .
$$

Note that $F$ is a CFP for both the bidirectional and the unidirectional case. Note also that, in the matrix representation, regular links "correspond" to either consecutive elements on the same row or to elements ( $W[h, g-1], W[h+1,0]$ ), while bypass links "correspond" to consecutive elements in the same column.

CFP's can be characterized, with respect to the above matrix representation, as follows:

Theorem 4 ([4]) Necessary and sufficient condition for a fault pattern $F$ of cardinality $g$ to be catastrophic for a bidirectional linear array with link redundancy $g$ is $W[0,0]=W[0, g-1]=1$ and, for any $1 \leq k \leq g-2$,

- if $W[h, k-1]=1$ then exactly one among $W[h-1, k], W[h, k]$ and $W[h+1, k]$ (whenever such elements are defined) is 1.
- if $W\left[h^{\prime}, k+1\right]=1$ then exactly one among $W\left[h^{\prime}-1, k\right], W\left[h^{\prime}, k\right]$ and $W\left[h^{\prime}+1, k\right]$ (whenever such elements are defined) is 1 .

Theorem 5 ([4]) Necessary and sufficient condition for a fault pattern $F$ of cardinality $g$ to be catastrophic for a unidirectional linear array with link redundancy $g$ is $W[0,0]=W[0, g-1]=1$ and, for any $1 \leq k \leq g-2$,

- if $W[h, k-1]=1$ then at least one among $W[h-1, k], W[h, k], \ldots, W\left[\bar{W}_{F}^{U}-1, k\right]$ (whenever such elements are defined) is 1 .
- if $W\left[h^{\prime}, k+1\right]=1$ then at least one among $W[0, k], W[1, k], \ldots, W\left[h^{\prime}+1, k\right]$ (whenever such elements are defined) is 1 .

Note that the matrix representation of any CFP $F$ with $|F|=g$ must have exactly one element $W\left[h_{k}, k\right]=1$ for each column $k, 0 \leq k \leq g-1$ and that any CFP for the bidirectional case is also a CFP for the unidirectional case.

Using the above two theorems, we can represent each minimal CFP starting at any fixed $p_{i}$, with $i \leq N-W_{F}^{x}$, as a sequence of $g-1$ integer "moves" $m_{j}$, with $1 \leq j \leq g-1$, where each $m_{j}$ indicates the increment of the row index from the (only) element set to 1 in column $j-1$ to the (only) one in column $j$ in the associated matrix $W$. More formally, let $W\left[h_{i-1}, i-1\right]$ and $W\left[h_{i}, i\right]$ both be 1. Then $m_{i}=h_{i-1}-h_{i}$. As an example, the sequence associated to the matrix in (1) is $\langle-1,-1,1,1\rangle$.

We will refer to sequences representing CFP's as catastrophic sequences. For minimal CFP's starting at any fixed position $i \leq N-W_{F}^{x}$, catastrophic sequences can be characterized as follows:

Proposition 1 Let $\left\langle m_{1}, m_{2}, \ldots, m_{g-1}\right\rangle$ be a sequence of moves such that:

- $-1 \leq m_{i} \leq 1$ for $1 \leq i \leq g-1$;
- $\sum_{i=1}^{k} m_{i} \leq 0$ for any $1 \leq k \leq g-2$;
- $\sum_{i=1}^{g-1} m_{i}=0$.

Then any such sequence corresponds to a minimal CFP for the bidirectional case and vice versa.

Proposition 2 Let $\left\langle m_{1}, m_{2}, \ldots, m_{g-1}\right\rangle$ be a sequence of moves such that:

- $m_{i} \leq 1$ for $1 \leq i \leq g-1$;
- $\sum_{i=1}^{k} m_{i} \leq 0$ for any $1 \leq k \leq g-2$;
- $\sum_{i=1}^{g-1} m_{i}=0$.

Then any such sequence corresponds to a minimal CFP for the unidirectional case and vice versa.

The above propositions follow by observing the immediate bijection between catastrophic sequences and matrix representations of minimal CFP's

Let us now come to bounding the number $\mathrm{F}^{U}(g)$ of catastrophic fault patterns starting at any $p_{i}, 0 \leq i \leq N-W_{F}^{U}$ under the assumption of unidirectional links. Clearly, $\mathrm{F}^{U}(g)$ is lowerbounded by $\mathrm{F}^{B}(g)$. In order to determine an upper bound to $\mathrm{F}^{U}(g)$, we establish a mapping from unidirectional catastrophic sequences to bidirectional ones. Let $S_{g}^{B}$ (resp., $S_{g}^{U}$ ) be the set of catastrophic sequences (of length $g-1$ ) for bidirectional (resp., unidirectional) linear arrays with link redundancy $g$. Moreover, let $\bar{S}_{g}^{U} \subset S_{g}^{U}$ be the subset of unidirectional catastrophic sequences $\left\langle m_{1}, m_{2}, \ldots, m_{g-1}\right\rangle$ such that $m_{i} \neq 0$, for $1 \leq i \leq g-1$. We have:

Lemma $1\left|\bar{S}_{g}^{U}\right|<F^{B}(2 g-1)$.

Proof: Any sequence $\left\langle m_{1}, m_{2}, \ldots, m_{g-1}\right\rangle$ belonging to $\bar{S}_{g}^{U}$ can be transformed into one for the bidirectional case by substituting each $m_{i} \leq-2$ with a string $m_{i_{1}}, m_{i_{2}}, \ldots, m_{i_{l}}$ with $l=\left|m_{i}\right|+2$, $m_{i_{1}}=m_{i_{l}}=0$ and $m_{i_{j}}=-1$ for $2 \leq j \leq l-1$. It is straightforward to see that the new sequence satisfies the properties given in Proposition 1 for a given $g^{\prime} \geq g$. We are then left with bounding $g^{\prime}$. Note that $g^{\prime}-1=g-1+\left|\sum_{m_{i} \leq-2} m_{i}\right|+n$, where $n$ is the number of terms in the summation. Given that

$$
0=-\left|\sum_{m_{i} \leq-2} m_{i}\right|+\sum_{m_{i}>-2} m_{i} \leq-\left|\sum_{m_{i} \leq-2} m_{i}\right|+g-1-n
$$

we have that $n+\left|\sum_{m_{i} \leq-2} m_{i}\right| \leq g-1$. Therefore $g^{\prime} \leq 2 g-1$. In order to obtain the desired mapping, we simply "pad" each transformed sequence of length $<2 g-1$ with zeroes. It is immediate to see that the obtained mapping is injective. The lemma follows.

Theorem $6 F^{U}(g) \in O\left(\frac{10^{g}}{g^{3 / 2}}\right)$.

Proof: Any catastrophic sequence in $S_{g}^{U}$ (except for the sequence of $g-1$ zeroes) can be univocally obtained by interleaving a catastrophic sequence in $\bar{S}_{j}^{U}, 3 \leq j \leq g$, with $g-j$ zeroes. Vice versa, any interleaving of a given catastrophic sequence in $\bar{S}_{j}^{U}, 3 \leq j \leq g$, with $g-j$ zeroes yields a catastrophic sequence in $S_{g}^{U}$ different from the sequence of $g-1$ zeroes. Therefore, from Lemma 1:

$$
\begin{aligned}
\mathrm{F}^{U}(g) & =1+\sum_{j=3}^{g}\left|\bar{S}_{j}^{U}\right|\binom{g-1}{g-j} \\
& \leq 1+\sum_{j=3}^{g} \mathrm{~F}^{B}(2 g-1)\binom{g-1}{g-j}
\end{aligned}
$$

By applying theorem 3, after some trivial manipulations we can determine a small constant $c>0$ such that

$$
\mathrm{F}^{U}(g) \leq 1+c \sum_{j=2}^{g-1} \frac{3^{2 j}}{j^{3 / 2}}\binom{g-1}{j} \in O\left(\frac{10^{g}}{g^{3 / 2}}\right)
$$

## 4 Application

In the previous sections, we have proved bounds on the number of minimal Catastrophic Fault Patterns (CFP's) for both bidirectional and unidirectional arrays with link redundancy $g$. Our bounds are tight for the bidirectional case.

The study of minimal CFP's for such architectures has received vast attention in the open literature. In particular, efficient testing algorithms have been devised [5] to detect the presence of such patterns. Restricting attention to minimal CFP's is justified by the fact that, in VLSI manufacturing, expensive on-chip reconfiguration is attempted only when the number of faults does not exceed a certain threshold. Otherwise, the defective chip is simply discarded [2].

The study of minimal CFP's relates to the case when reconfiguration is attempted only if at most $g$ faults are detected. A measure of interest is then the fraction $f$ of chips for which no reconfiguration strategy is successful: bounds on $f$ are useful when assessing the cost of the manufacturing process. Note that $f$ is the probability of the following event:
$\mathbf{C}=$ "There are exactly $g$ faulty PE's in the array which disconnect node $I$ from node $O$ ".

We evaluate $f$ under a well established probabilistic framework, where each PE has an independent probability $p$ of being faulty and $(1-p)$ of being operating correctly [2]. Let CFP be the event: " $g$ faulty PE's in the array form a CFP" and G the event: "There are exactly $g$ faulty PE's". Then

$$
f=\operatorname{Pr}(\mathbf{C})=\operatorname{Pr}(\mathbf{C F P} \cap \mathbf{G})=\operatorname{Pr}(\mathbf{C F P} \mid \mathbf{G})\binom{N}{g} p^{g}(1-p)^{N-g}
$$

We are then left with bounding $\operatorname{Pr}(\mathbf{C F P} \mid \mathbf{G})$ for both the bidirectional and unidirectional case. Let $\mathrm{F}_{j}^{x}(g), x \in\{B, U\}$, be the number of CFP's starting at any $p_{j}$ with $0 \leq j \leq N-g$. Clearly,

$$
\begin{equation*}
\operatorname{Pr}(\mathbf{C F P} \mid \mathbf{G})=\frac{\sum_{j=0}^{N-g} \mathrm{~F}_{j}^{x}(g)}{\binom{N}{g}} \leq \frac{(N-g) \mathrm{F}^{x}(g)}{\binom{N}{g}} \tag{2}
\end{equation*}
$$

since any $\mathrm{F}_{j}^{x}(g), 0 \leq j \leq N-g$, can be upper bounded by $\mathrm{F}^{x}(g)$ (note that the above bound is particularly accurate for $N \gg g$ ). By expanding (2) on the basis of Theorems 3 and 6 we then have, for the bidirectional case,

$$
\operatorname{Pr}(\mathbf{C}) \in O\left(\frac{N(3 p)^{g}(1-p)^{N-g}}{g^{3 / 2}}\right)
$$

Finally, for the unidirectional case, we have

$$
\operatorname{Pr}(\mathbf{C}) \in O\left(\frac{N(10 p)^{g}(1-p)^{N-g}}{g^{3 / 2}}\right)
$$

## References

[1] K.P. Brlkhale and P. Banerjee. Reconfiguration Strategies in VLSI Processor Arrays. in Proc. Int'l Conf. on Computer Aided Design (1988) pp.418-421.
[2] J.W. Greene and A. Gamal. Configuration of VLSI Arrays in the Presence of Defects. Journal of the ACM 31(4) (1984) pp.694-717.
[3] R.L. Graham, D.E. Knuth and O. Patashnik. Concrete Mathematics. Addison Wesley, Reading Mass. (1989).
[4] A. Nayak. On Reconfigurability of some Regular Architectures. PhD Thesis, Dept. of Systems and Computer Engineering, Carleton Univ. Ottawa Canada (1991).
[5] A.Nayak, L. Pagli and N. Santoro. Testing for Catastrophic Fault Patterns in Reconfigurable Arrays with Arbitrary Link Redundancy. Submitted to IEEE Trans. on Computer Aided Design.
[6] A. Nayak, N. Santoro and R. Tan. Fault-Intolerance of Reconfigurable Systolic Arrays. In Proc. 20th Int. Symp. on Fault Tolerant Computing, FTCS'20 (1990) pp. 202-209.


[^0]:    *This work has been supported by the C.N.R. project "Sistemi Informatici e Calcolo Parallelo".
    ${ }^{\dagger}$ Dipartimento di Informatica, Università di Pisa, 56124 Pisa, Italy.
    ${ }^{\ddagger}$ Dipartimento di Elettronica e Informatica, Università di Padova, 35131 Padova, Italy, and International Computer Science Institute, Berkeley, CA 94704-1105, USA.

